



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Gary P. Morrison, et al.**

Docket: **TI-31373**

Serial No.: **10/034,827**

Art Unit: **2827**

Filed: **01/03/2002**

Examiner: **Mitchell, James**

For: **Chip-Scale Packages Stacked on Folded**

Conf. No.: **4496**

Interconnector For Vertical Assembly

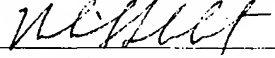
On Substrates

EXTENSION OF TIME

Assistant Commissioner For Patents

Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)	
I hereby certify that on this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.	
	<u>2-21-03</u>
Michael Skrehot, Reg. 36,682	Date

Pursuant to 37 CFR 1.136(a), Applicants respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

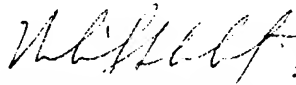
The fee for this extension is indicated below:

<input checked="" type="checkbox"/> One Month	(\$110)	<input type="checkbox"/> Four Months	(\$1,450)
<input type="checkbox"/> Two Months	(\$410)	<input type="checkbox"/> Five Months	(\$1,970)
<input type="checkbox"/> Three Months	(\$930)		

Any further necessary extension of time is hereby requested. Charge any and all fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0068. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated
Mail Station 3999
P.O. Box 655474
Dallas, TX 75265
(972) 917-5653

Respectfully submitted,


Michael K. Skrehot
Registration No. 36,682

RECEIVED
MAR 11 2003
TECHNOLOGY CENTER 2809